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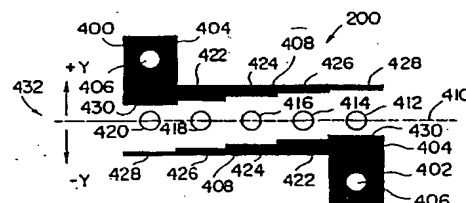
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(54) **System for measuring misregistration.**

(57) The present invention relates to a system for measuring the amount and direction of misregistration of each layer of a panel from which multi-layer printed circuit boards are separated. The system utilizes a conductor pattern having a conductive trace positioned on each layer of the panel. Each conductive trace includes two trace portions 400 and 402, positioned in opposing relation with a plurality of conductive through-holes 412, 414, 416, 418, and 420, being positioned therebetween. Each of the trace portions 400 and 402 includes a plurality of elongate conductor portions 408 positioned incrementally varying distances from a common axis. The conductive through-holes 412, 414, 416, 418, and 420 are formed in the panel after the layers have been fabricated and joined. If the through-holes 412, 414, 416, 418 and 420 are in electrical contact with either trace portion of 400 or 402, then the amount of misregistration can be determined. Further, by determining which trace portion the through-hole is in contact with, the direction of misregistration can be determined. By adding additional trace portions, the quantity of misregistration in other directions may be determined.

[FIG. 4]



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BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates generally to printed circuit boards, and more particularly, to an improved system for measuring the amount of misregistration of printed circuit board layers.

Related Background Art

In fabrication of printed circuit boards using multiple layers, each of the layers is fabricated in face-to-face juxtaposition and has electrically conductive traces thereon which require interconnection between the layers. Such interconnection is usually accomplished by a drilled and then metallic-plated through hole which extends through layers and is located to cause the plated through-hole to contact the particular traces or pads thereof on one or more of the layers and create an electrically conductive path between the desired traces on different layers. For such a fabrication technique to be effective, it is very important that each of the inner layers of the printed circuit boards be in precise registration or alignment with an exterior layer of the printed circuit board so that the relative positions of the traces on different layers are predictable and the through holes, which are formed after assembly of the layers, will make contact with the desired traces and only those traces. If an inner layer registration with the exterior layer or with any of the other interior layer by too large of an amount, the through-hole will not contact the desired trace, or will contact an undesired trace, and a defective board will result.

As with most fabrication processes, perfect registration of inner layers with the exterior layer is not possible, and the size and placement of the traces must be designed to take into account the fact that some misregistration is inevitable. The larger the amount of misalignment anticipated, the larger the spaces between traces must be to ensure that the through-hole will contact only the desired traces. This reduces the number of electronic devices which may be placed on the printed circuit board, commonly referred to as the packing density of the board, and hence increases the cost of the printed circuit boards required for a particular circuit.

In the past, after the printed circuit board was fabricated, it was possible to determine whether or not the layer-to-layer registration was correct or incorrect by simply determining whether or not the through-holes made electrical contact with all of the proper traces and no other. Usually, a particular through-hole was checked to make sure it was in contact with a particular trace, and if it was not, the board failed. On the other hand, if all of the through-holes, or however many that were checked, contacted all of the desired

traces, the board passed. Rather than checking all through-holes, a sampling was often made. At other times, a test set of traces and through holes (called a "test coupon") was created for the purpose of testing registration. In all situation, the test simply produced a "pass" or "no pass" result, without any quantification of the amount of layer misregistration involved.

Of course, the inner layers of the fabricated printed circuit board can not be viewed to visually determine the amount of misregistration, at least not without destroying the board by cutting it part.

Thus, if a particular board passed the test, there was no guarantee that the board would not later fail due to thermal changes or mechanical flexing which might be sufficient to cause a through-hole to lose electrical contact with a desired trace or make electrical contact with an undesired trace. In other words, one could never be sure that a board which passed the registration test did not have marginally adequate layer-to-layer registration and would not later fail during use. As a result, larger-than-necessary allowances in size and position of trace and spaces therebetween were made to lessen the likelihood that a board that passed a registration test would subsequently fail during use because the layer-to-layer registration was barely within tolerance.

The packaging densities of printed circuit boards have increased dramatically, with more and smaller components requiring increased number of interconnecting traces. This requires that the traces be reduced accordingly. To assure accuracy in fabrication, the phototooling, image transfer, etching, drilling, lamination and other processes involved in the fabrication of printed circuit boards must controlled within ever tighter tolerance limits. As the board manufacturers approach the inherent accuracy limitations on these processes, it becomes critical to quantitatively measure the amount of misregistration between layers rather than simply conducting a pass/non-pass registration test. While such knowledge is helpful, not knowing the amount by which the layers are out of registration, it is impossible to determine whether through-holes are making adequate electrical contact with traces or whether minimum electrical clearances are being maintained between through-holes and traces on the hidden inner layers of the printed circuit board.

Further, in those applications where failure costs are unacceptably high, as in printed circuit boards used in life support systems and military equipment, over designing into additional layers and higher cost will not necessarily prevent the recurrent misregistration which results from a failure to identify and cure process failures. In this regard, many boards may be produced which will fail the "pass" or "no pass" test leading to further added expenses. Such a system requires not only knowledge of the amount of failure but also the direction of misregistration, that is whether a

layer is displaced to the right, left, forward or backwards. Further, such a system requires knowledge of the misregistration of each board regardless of the quantity of misregistration of other boards.

It will therefore be appreciated that there has been a significant need for a system for measuring the amount and direction of misregistration separately for each printed circuit board layer in quantitative terms.

The system should allow the measurement of misregistration to be conducted quickly, economically, and precisely.

SUMMARY OF THE INVENTION

The present invention comprises;

a plurality of layers each having electrically conductive traces which require interconnection between said layers, said layers including an exterior layer and a plurality of inner layers inwardly located relative to said exterior layer, said layers being positioned in registration with each other within a predetermined maximum tolerance range;

a plurality of electrically conductive quantifying conductor means for providing a path of electrical conductivity to said inner layer,

said quantifying conductor means comprising first conductor portion including a plurality of conductors aligned along the virtual first axis crossing the measuring direction, and also at least one common conductor;

registration trace means formed upon said inner layer for cooperating with said quantifying conductor means to determine the direction and quantity of misregistration of said inner layer with said exterior layer, said trace means having a common portion adapted to be in electrical contact with said at least one common conductor whether or not said inner layer is in registration with said exterior layer,

said trace means having a quantifying trace portion adapted to produce a plurality of incrementally varying clearances between said quantifying conductor means and said trace means if said inner layer is in perfect registration with said exterior layer, said quantifying conductors being out of electrical contact with said trace means if said inner layer is in perfect registration with said exterior layer, and one or more of said quantifying conductors being in electrical contact with said trace means if said inner layer is out of registration with said exterior layer wherein electrical contact of said trace means with said first portion of conductors indicates misregistration in the measuring direction.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is an illustration of a panel containing a plurality of printed circuit boards wherein the pan-

el includes structure which is the subject of the present invention;

FIG. 2 is an enlarged top plan view of the trace means that is attached to each of the inner layers of the panel of FIG. 1 prior to assembly of the layers;

FIG. 3 is a fragmentary view of the panel of FIG. 1 taken along through holes;

FIG. 4 is an enlarged plan view of a trace portion illustrating the manner of quantification of misregistration of the layers in accordance with the first embodiment of the present invention;

FIG. 5 is a perspective plan view of the panel of FIG. 1 together with the testers for illustrating alternative methods for sensing conduction to thereby determine misregistration;

FIG. 6 is a schematic diagram of the circuitry for the testers shown in FIG. 5;

FIG. 7 is an enlarged top plan view of a trace portion which can be used for the first modification example of the present invention;

FIG. 8 is an enlarged top plan view of a trace portion which can be used for the second modification example of the present invention;

FIG. 9 is an enlarged top plan view of a trace portion which can be used for the third modification example of the present invention;

FIG. 10 is an enlarged top plan view of a trace portion which can be used for the fourth modification example of the present invention;

FIG. 11 is an enlarged top plan view of a trace portion which can be used for the fifth modification example of the present invention;

FIG. 12 is an enlarged top plan view of a trace portion which can be used for the sixth modification example of the present invention;

FIG. 13 is an enlarged top plan view of a trace portion which can be used for the seventh modification example of the present invention; and

FIG. 14 is an enlarged top plan view of a trace portion which can be used for the second embodiment of the invention to be attached to each of the inner layers of the panel of FIG. 1, prior to assembly of the layers.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

One of the best mode for carrying out the invention will be discussed hereunder as shown in the attached drawings. In the discussion a single symbol is always used for the identical element and repetitive explanation shall be omitted.

As shown in the drawings for the purpose of illustration, the present invention is embodied in a system for measuring the amount of misregistration of printed circuit board layers. The system is particularly adapted for use with a panel, indicated generally by refer-

ence numeral 100, FIG. 1, that includes a plurality of printed circuit boards 102, 104, 106 and 108 contained thereon. The panel 100 is fabricated from a plurality of layers using known techniques to provide the plurality of printed circuit boards. The board 102, 104, 106, and 108 may be separated from the panel, also using known techniques.

As shown in FIG. 1 for the uppermost layer of the circuit board 106, each of the circuit board layers, and hence the panel layers, has electrically conductive traces 110 which require interconnection between the layers. As shown in FIG. 3, the layers of the panel 100 include an uppermost exterior layer 112 and a plurality of inner layers 114, indicated by the reference numerals 114a, 114b, and 114c. While the inner layer 114c is referred to as an "inner layer" relative to the exterior 112, it is actually at the lowermost position of the panel 100. While the panel 100 is shown in the drawings as being a four-layer panel, the present invention can be practiced with panels of any number of layers. However, it will be apparent to those skilled in the art that each of the printed circuit boards 102, 104, 106, and 108 of the panel 100 must have the same number of layers.

The layers 112 and 114a-c are positioned in registration with each other within a predetermined maximum tolerance range. If the layers are grossly out of registration, the present invention will not function. However, misregistration by such a large amount would be evident from visual inspection. The more difficult problem, and the one solved by the present invention, involves the situation in which the hidden inner layers are out of registration with the exterior layer, and hence each other, by approximately 1/50 mm, which is not enough to be visually detectable, but yet is beyond an acceptable tolerance limit.

With the present invention, it is possible to determine not only whether or not the inner layers are in registration within a desired tolerance limit, but also the quantitative amount of layer misregistration that exists and the direction of misregistration. It is possible to determine the accuracy of the misregistration within 1/50 mm, or less if desired.

The present invention uses an electrically conductive, thin metal sheet or film, commonly referred to as a registration check plane, attached to each of the inner layers 114a-c, the registration check planes of each layer cooperating with a conductor formation 116 to provide a registration check system. The conductor formation 116 comprises a plurality of sets of trace conductors 120, 122, and 124, each in electrical contact with the check plane of a layer of the panel 100. The registration check plane may be deposited and shaped on the layers using conventional printed circuit board fabrication techniques as part of the process used to form the traces 110. Hence, the registration check planes can be added during the design phase to insure proper placement on the inner layers

relative to the traces, and formed using a conventional film and etching process. The traces are formed with almost no extra cost or effort involved.

It is desirable to locate the check planes on the inner layers 114a-c about the periphery of the panel 100 as is illustrated by the trace conductors 120, 122 and 124. As is shown in FIG. 1, a conductor formation 116 may be placed in each corner of the panel 100. Further, conductor formations may be placed on the panel 100, such as on the interior thereof, as is the conductor formation 118. In the presently preferred embodiment of the invention, however, it is desirable to keep the check planes from contacting and becoming a portion of the layers of the boards 102, 104, 106, and 108.

Each set of trace conductors 120, 122 and 124 have a generally "V" shape, and the plurality of trace conductors 120, 122, and 124 are positioned in diagonal alignment in the corner of the panel 100. It is desirable to position conductor formations, and hence the underlying check planes, at opposite corners of the panel to achieve a measure of misregistration for the panel representative of all portions thereof.

To determine the misregistration direction and quantify the misregistration amount, it may be desirable to place the conductor formations 116 in each corner of the panel 100 as shown in FIG. 1. As will become apparent throughout the discussion below, it is not necessary that the trace conductors be diagonally aligned, but this formation is desirable to allow compact spacing of the trace conductors and underlying check planes. However, it is desirable that the trace conductors and the underlying check planes have a "V" shape to allow quantification of misregistration in orthogonal directions, as will also become apparent below.

Each set of trace conductors 120, 122 and 124 of the conductor formation 116 corresponds to one of the check planes located on one of the interior layers of the panel 100. In the embodiment being illustrated herein, three interior layers 114a-c require three check planes for determining the registration of each layer. The traces for the check planes would each be fabricated on one of the interior layers to determine that layer's misregistration as will be described in more detail below. Accordingly, a trace 120a would be fabricated upon the layer 114a to produce its check plane, a trace 122a fabricated upon the layer 114b and a trace 124a fabricated upon the layer 114c.

The trace 120a is shown in FIG. 2 removed from the inner layer 114a to which it is attached to practice the invention. The traces 122a and 124a associated with the layer 114b and 114c, respectively, are constructed and function in a manner similar to the trace 120a. Accordingly, only the trace 120a will be described in detail herein. The trace 120a comprises two substantially identical trace portions 200 and 202 positioned in orthogonal relation to provide the gen-

eral "V" shape of the trace 120a. The trace portion 200 is illustrated in more detail in FIG. 4. It will be apparent, however, to those skilled in the art that the trace portion 200 will be described in detail herein.

The trace portion 200 includes two substantially identical trace conductor members 400 and 402 positioned in opposing relation. Each conductor member includes a ground, or common, portion 404 which is made of substantial size to insure contact with a through-hole 406 formed in the center thereof. Although the traces 120a, 122a and 124a are formed during fabrication of each layer as part of the overall deposit and etch process, the through-hole 406 is formed after the panel 100 is complete. A conductor is formed in the through-hole, as will be described in more detail below. The common portion 406 is therefore sized to be in electrical contact with the conductor of the through-hole 406 taking into consideration the maximum misregistration possible.

The conductor members 400 and 402 include elongate conductor portions 408 each having a thickness which decreases incrementally. The conductor member 400 and 402 are each positioned in relation to a common axis 410 such that the elongate conductor portions thereof have incrementally varying clearances from the axis 410. A plurality of through holes 412, 414, 416, 418 and 420 are each drilled along the axis 410 after completion of the panel 100 in a manner similar to the through hole 406. Accordingly, since the elongate conductor portion 400 of the conductor members 400 and 402 have incrementally varying clearances from the common axis 410, they also have incrementally varying clearances from the through-holes 412, 414, 416, 418 and 420.

In the presently preferred embodiment of the invention the incremental variation in the clearance from the axis 410 is provided by fabricating the elongate portion 408 of a plurality of elongate conductor segments 422 through 420 each of varying thickness so that the thickness of the elongate conductor portion 408 decreases incrementally.

The conductor segments cooperate with the through-holes to quantify the misregistration of the layer 114a of the panel 100. That is, since the through-holes 412, 414, 416, 418 and 420 are formed in the panel after the trace 120, contact of any through-hole 412, 414, 416, and 418 with the conductor member 400 will indicate misregistration of the layer 114a of the panel. Since the amount of clearance, (which should be present in absence of any misregistration), between any through hole 412, 414, 416, 418 and 420 and its respective elongate conductor segment 422 through 430 of the conductor member 400 is known, then the quantity of misregistration can be determined by determining which through-hole is in contact with the conductor member 400.

For example, assume the clearance between the conductor segment 430 of the conductor member 400

and the through-hole 420 is 2/50 mm when there is no misregistration, and the amount of clearance increments 2/50 mm for each elongate conductor segment 422, 424, 426 and 428 so that the amount of clearance between the elongate conductor segment 428 and the through-hole 412 is 10/50mm. If, after conductors are formed in the through-holes the through-hole 418 is in electrical contact with the conductor member 400 but the through-hole 416 is not, then the amount of misregistration is between 4/50 and 6/50 mm. That is, the board is out of registration at least 4/50 mm but less than 6/50 mm in a direction orthogonal to the axis 410.

Further, the two conductor member 400 and 402 cooperate to determine the direction of misregistration. That is, in the example given above, if the conductor of the through-hole 418 is in contact with the conductor member 400 then the layer 114a is out of registration at least 4/50 mm in the positive "Y" direction with respect to the coordinates 432. Conversely, if the through-hole 414 is in contact with the conductor member 402 then the layer 114a is out of registration at least 4/50 mm in the minus "Y" direction with respect to the coordinate system 432. In similar manner, the trace portion 202 determines the direction of misregistration of the layer 114a in the "X" direction with respect to the coordinates 432. The trace portion 202 also quantifies the amount of misregistration in each the positive and negative "X" direction.

As used herein, the length of the elongate conductor segments 422, 424, 426, 428 and 430 refers to the dimension perpendicular to the measuring direction of the subject trace portion. That is, the trace portion 200 measures misregistration in the "Y" direction so that the length of the elongate conductor segments 422, 424, 426, 428, 430 and the common portion 404 to the trace portion 200 is measured in the "X" direction. It will be apparent to those skilled in the art that the length of the elongate conductor segments must be large with respect to the maximum expected misregistration. This will assure that contact of any of the through-holes 412, 414, 416, 418, 420 with the conductor members 400 or 402 of trace 200 indicates precise misregistration in the presence of displacement of the layer 114a in the "X" direction, i.e., "X" direction misregistration. In other words, this will assure that the through-hole 416 will never contact the elongate conductor segments 422 and 426.

Further, it will be apparent to those skilled in the art that although the incremental variation in clearance is provided by providing a stepped conductor member 400 in the present invention, other variations may be provided, such as an angled conductor to create a continuously varying clearance. Alternately, the invention may be practiced by incrementally varying the diameter of the through-holes 412, 414, 416, 418, 420 and placing them between two traces with parallel straight edges. However, the stepped conductor

of the present invention is easier to fabricate since the through-holes are drilled on a straight placement line and have constant diameter, and, therefore preferred to an angled conductor or varied hole-diameter arrangement. Still further, while the trace portions 200 and 202 are illustrated herein as comprising a pair of conductor members in opposing relation with a plurality of through-holes positioned therebetween, those skilled in the art will appreciate that the invention will be equally workable with a pair of aligned through-holes in opposing relation with a single conductor therebetween and wherein each side of the conductor is shaped to incrementally vary the clearances from its respective set of aligned through-holes. Many other variations for the through holes and conductors will become apparent to those skilled in the art.

The minimum amount of clearances which can be measured in the presently preferred embodiment of the invention is 2/50 mm with increment of 2/50 mm. However, it will be appreciated that minimums of any amount may be chosen with increments of any amount. Accordingly, a minimum of 1/50 mm with increment of 1/100 mm may be selected. The only limitation on the minuteness of minimum or incremental measurement is the limitation on ability to fabricate the conductor portions. Further, the clearance, or increment thereof, need not to be uniform along the length of the conductor members nor need it be identical for each trace segment. Variation of the increment between trace segments will allow variable measurement of the misregistration in the "X" and "Y" directions.

It will also be apparent to those skilled in the art that the placement of the traces 120a, 122a and 124a must be controlled and that the positioning of the through-holes for the trace conductors 120, 122 and 124 must be formed with accuracy. This is necessary to insure the integrity of the measurement. Like the trace 120a, the traces 122a and 124a indicate and quantify the misregistration of the layers 114b and 114c, respectively, independent of the registration, or lack thereof, of any other layer.

With the exterior layer 112 and the inner layers 114a-c laminated together, the through-holes 412 through 420 and 406 are formed for each of the traces 120a, 122a and 124a for each of the check planes of each layer 114a-c. Each through-hole 412 through 420 and 406 for each trace portion of each trace 120a, 122a and 124a for each layer 114a-c is formed in a similar manner as will be described by reference to through-holes 406 and 412 shown in FIG. 3. Each through-hole 406 and 412 is provided with a conductor 300 which is formed on the side wall of the through-hole using known plating techniques. Each conductor 300 includes an electrically conductive contact pad 302 attached to an exposed exterior surface 304 of the exterior layer 112. The contact pads

302 are circular and are arranged in a contact pad placement pattern corresponding to the placement pattern for the through holes 412 through 420 and 406 for each trace portion 200 and 202. The contact pads 302 for each trace portion 200 and 202 being placed in the orientation to create the trace conductors 120, 122 and 124. Further, the contact pads of each trace 120a, 122a and 124a of conductor formation 116 of each plane will be oriented to form the pattern as illustrated in FIG. 1.

As noted above, the conductors 300 are formed by drilling a hole through the layers 112 and 114a-c and then plating the hole to form a conductor with the shape of a barrel. The holes are drilled through the contact pads 304, which are typically placed prior to drilling, and when the drilled hole is plated to form the conductors 300 the plating adheres to the corresponding contact pads 302. The contact pads serve to prevent the conductors 300 from falling out of the drilled holes, and also as electrical contact points on the exterior surfaces of the board. It will be apparent that the pads 306 may be provided on the reverse side of the panel 100, but only pads 302 are necessary to practice the present invention.

With the arrangement described above, when any one of the inner layers 114a-c is out of registration with the exterior layer 112 by a sufficiently large amount, one or more electrically conductive paths are established between the contact pads 302 of the through-holes 412 through 420 and the contact pad 300 of the through-hole 406 for that layer. Electrical continuity may be determined by use of a continuity tester 500, such as will be described below. Each of the electrically conductive paths established is through a conductor 300 of one of the through-holes 412 through 420 thereby indicating that the through-hole is in contact with the conductor member 400 or 402 and further indicating the quantity and direction of misregistration.

The tester 500, shown in FIG. 5, is a hand held device with a case 502 and a head 504 such as that shown and described in the above-references U.S. Application Ser. No. 216,080 entitled SYSTEM FOR MEASURING MISREGISTRATION OF PRINTED CIRCUIT BOARD LAYERS filed July 7, 1988 by Tom R. Paur, the disclosure of which is incorporated herein, in its entirety, by the foregoing reference thereto. Mounted within the case is a battery 600, shown schematically in FIG. 6. The case 502 also supports five light-emitting diodes 602, 604, 606, 608 and 610 which produce visible light when activated.

The head 504 of the tester 500 has mounted thereon five axially spring-loaded probes 50a, 50b, 50c, 50d and 50e, and an axially spring-loaded probe 52. The probes 50a-e and the common probe 52 having a placement pattern corresponding to the placement pattern for the contact pads 300 of the through-holes 412 through 420 and 406 of conductor member

400 of the trace portion 200, which is the same pattern for the conductor member 402, and for the conductor members of the other trace portion 202. With such a placement pattern for the probes on the head 504, the tester may be conveniently grasped by the user and held with the probes in electrical contact with the contact pads and common pad to accomplish the continuity check referred to above. The user first checking for continuity between any of the through-holes 412 through 420 and the through-hole 406 of the conductor member 400 to determine the quantity of misregistration of the layer 114a in the positive "Y" direction and then checking the continuity between any of the through-holes 412 through 420 and the contact pad of the through-hole 406 of the conductor member 402 to determine the quantity of misregistration in the negative "Y" direction. This process would be repeated for the trace portion 202 to determine the quantity of positive and negative "X" direction misregistration of the layer 114a and the entire process repeated for layers 114b and c.

As shown in FIG. 6, the anode of each of the light emitting diodes 602-610 is connected to a corresponding one of the probes 50a-e, and the cathodes of the light-emitting diodes are connected together and to the positive terminal of the battery 600. The negative terminal of the battery is connected to the common probe 52. As such, if one or more of the conductors 300 of the through-holes 412 through 420 are in contact with the conductor 300 of a through-hole 406 as a result of misregistration of an inner layer, a complete circuit is accomplished and current flows through the corresponding light-emitting diodes.

In the example given above, the light-emitting diodes 610 and 608 would be illuminated to indicate electrical contact between the contact pads 300 of the through-holes 420 and 418 while no other light emitting diode would be illuminated.

The illumination of any light emitting diodes 602-610 indicates that the subject layer, layer 114a in our examples, is out of registration with the exterior layer 112 by at least the minimum clearance amount. Of course, if the misregistration in the positive "Y" direction were of sufficient enough size that the conductor of through-hole 412 were in electrical contact with the elongated conductor portion 428, the light-emitting diode 602 would also be illuminated and the user would know that layer 114a was out of registration by more than the clearance of the elongate portion 428 from the through-hole 412.

With the present invention, the quantitative amount of misregistration can be measured very precisely, easily, and economically. Further more, not only can one determine if the printed circuit board is within a desired tolerance limit, it can be determined how close each of the inner layers of the board are to the tolerance limit and in which direction the layer is out of registration. In such manner, printed circuit

boards that might test as satisfactory using a conventional pass/no-pass continuity test, but yet be out of registration by a sufficient amount that the board might fail during use, can be identified with the present invention. Further, since the present invention provides a means of very precisely determining the amount and direction of misregistration, the designers not need built extremely large safety margins into their design, as was the case when the precise amount of misregistration could not be measure. By placement of conductor formations 116 in each corner of the panel 100, in addition to the underlying traces of each layer, we can check the registration of each corner of each layer to determine the quantity and direction of any misregistration with respect to that corner. Accordingly, even if a layer is rotated about a particular corner, such that the misregistration of that corner may be within the established tolerance, the "rotational misregistration" will be detected when checking the misregistration of that layer with respect to another corner. Rotational misregistration is as common as linear misregistration but, could not be detected using conventional means. With the present invention, such rotational misregistration can be detected. It is to be understood that while a hand-held tester using light emitting diodes to provide a visual indication of the amount of misregistration has been described, the tester need not to be hand-held and may be incorporated into a computer-based, automatic test system in which the continuity check is accomplished automatically. In such a system, the results may be displayed or recorded in other manners or used in the process to automatically determine whether a printed circuit board is sufficiently in registration to be used. In this embodiment, as with a hand-held tester configuration, the light-emitting diodes can be replaced with a digital numerical display and associated circuitry.

However, the automatic computer based tester configuration lends particularly to identification of process errors and correction there of before panels are produced that are not within tolerance. An illustrative system is shown in FIG. 5. Therein an automated sensor element 508, which may be similar to the hand-held tester 500, is adapted to be automatically placed in contact with the panel 100. The motion control of the sensor 500 may be controlled by a variety of linear motion controllers or robots as is known in the art. While only one sensor is shown in FIG. 5, those skilled in the art will appreciate that a sensor 508 may be provided for each trace portion of the panel 100 thereby further enhancing automatic testing.

The sensor 508 is coupled to a process computer 510 to provide results of the continuity test thereto. The process computer may comprise a conventional control devices such as, microprocessor or other computing apparatus, interfaces for the sensors and

other control devices, memory, user interfaces (keyboards, screens, etc.) and other control circuitry. Further, to enhance automation, the process computer may comprise apparatus for controlling the motion of the sensors 508. Still further, apparatus may be provided for automatically moving the panel to be tested into position prior to lowering the sensors 508. The process computer 510 may also be adapted to control this motion.

During automated testing of misregistration, the process computer would not only be able to immediately determine any X-Y misregistration of each layer of the panel by amount and direction, but would also be able to record and plot the results for each layer of each panel produced during a particular period of time. The results may be monitored, or otherwise periodically checked, for patterns if misregistration or increasing misregistration in a particular direction. Alternatively, the process computer may be provided with maximum limits on misregistration (which would be less than any failing tolerances) and programmed to provide some indication such as a light or bell upon misregistration beyond the limit at which time the process may be corrected and production continued. Such a system will allow process control to discover and correct process errors thereby to prevent fabrication of failing printed circuit boards.

It will be appreciated that, although specific embodiment of the invention have been described herein for purposes of illustration, various modifications may be made without departing from the spirit and scope of the invention. Accordingly, the invention is not limited except as by the appended claims.

For examples, the modifications as described below are possible to be made. FIG. 7 through FIG. 14 are enlarged top plan views of the modification examples of the present invention. The modification examples of the invention will be discussed hereunder. Although the following examples of modification show the system for measuring the amount of misregistration in the "Y" direction, it is, of course, applicable also to the measurement of misregistration in the "X" direction.

The first modification example of FIG. 7 is different from the said embodiment of the invention by virtue of the trace conductor members 400 and 402, each having a adjacent portion facing to through-holes 412, 414, 416 and 418, which clearance vary linearly from the said through-holes. Also in this case, as described before, a pair of trace conductor members 400 and 402 are positioned along the "Y" direction, in opposing relation with a plurality of through-holes 412, 414, 416 and 418 positioned therebetween which are aligned in the "X" direction. If the clearance between through-hole 412 and trace conductor member 402 is 2/50 mm, the clearance between each of through-holes 414, 416, 418 and 420 and trace conductor member 402 will increase gradually to 4/50

mm, 6/50 mm, 8/50 mm and 10/50 mm, respectively. Reversely, the clearances between trace conductor member 400 and through-holes 412, 414, 416, 418 and 420 will reduce by 2/50 mm each. Therefore, the amount and direction of misregistration can be determined by means of detecting how many through-holes are in contact with a conductor member, and with which member (400 or 402) they are making contact.

The second modification example of FIG. 8 is different from the first modification example by virtue of through-holes 412, 414, 416, 418 and 420 being placed linearly to cross the "X" and "Y" directions. Since the basic principles of measurement is as same as described before, explanations shall be omitted.

The third modification example of FIG. 9 is different from the first modification example by virtue of conductor segments of trace conduct members 400 and 402 being curved in relation with the placement direction of through-holes 412, 414, 416, 418 and 420. In this case, the intervals of through-holes 412, 414, 416, 418 and 420 are not uniform in distance. However, the clearances between through-holes 412 through 420 and trace conductor member 402 gradually increase, while the clearances between the said through-holes with trace conductor member 400 gradually decrease. The fourth modification example of FIG. 10 is different from the modification examples described above by virtue of the trace conductor members 400 and 402, which have not identical shape, being used. In this case, the fourth modification example (FIG. 8) is used for trace conductor member 400, while an elongate triangle having its hypotenuse on the right hand side is used for the shape of trace conductor member 402. Accordingly, external diameters of the through holes 412, 414, 416, 418 and 420 become smaller gradually in the order of said through-holes. The clearances between through-holes 412 through 420 and trace conductor member 402 gradually increase in distance, while the clearances between 412 through 420 and trace conductor member 400 gradually decrease.

The fifth modification example of FIG. 11 comprises a single trace conductor member 400 being extended in the "X" direction, which is put between a pair of through-holes 412a, 414a, 418a and 412b, 414b, 418b, being placed along a pair of virtual parallel lines which incline a certain degree from the "X" direction. Since the clearance between the through-holes and trace conductor member 400 is as mentioned above, further explanation shall be omitted. The sixth modification example of FIG. 12 is different from the modification examples mentioned above by virtue of its use of a trace conductor member 400 which has the shape of a ladder. In this case, through-holes 412, 414, 416 and 418 are positioned inside the trace conductor member 400 so that the clearances between through-holes 412 through 418 and adja-

cent bottom edge portion of trace conductor member 400 gradually increase, and the clearances of through-holes 412 through 418 and adjacent top portion of trace conductor member 400 gradually decrease.

The seventh modification example of FIG. 13 uses a plurality of circular trace conductor member and each of the circular trace conductors has a through-hole in the center. In this case, each of circular trace conductor member 400a, 400b, 400c, 400d and 400e is in electrical contact with one common conductor (not shown in FIG. 13), and the amount of misregistration can be determined by detecting each electrical continuity of contact between the common conductor and through holes 412, 414, 416, 418 and 420. Followings are explanations about a system for measuring the amount of misregistration in relation with the second embodiment of the present invention, which discussion will be made in accordance with FIG. 14. The FIG. 14 shows an enlarged top plan view of the trace that is attached to each of the inner layers of the panel of FIG. 1, prior to assembly of the layers, and the trace can be used for the second embodiment of the invention.

This embodiment of the invention is different from the said first embodiment of the invention and also from the said modification examples by virtue of each one pair of trace conductors, as described in the fourth modification example, being positioned in the "X" and "Y" directions, and out of which trace conductors those for measuring the amount of misregistration in the negative directions (that is, -X and -Y directions) are connected to form a body. With this system, the number of trace conductors required can be reduced, which will work for the sake of cost reduction. Further, the number of times necessary to change probes of the tester can be reduced, which will also reduce the time to complete the measurement.

When the amount of misregistration is measured in the -Y direction, V-shaped trace conductor portion 204 together with through-holes 412y, 414y, 416y, 418y and 420y may be used, and when measured in the -X direction, V-shaped trace conductor member 204 with through-holes 412x, 414x, 416x, 418 and 420x may be used.

This system allows the measurement of misregistration to be conducted quickly, economically, and precisely.

Further, as this system can minimize the effect of misregistration of inner layers which disturb the production with high packing densities of printed circuit boards, it will be possible to avoid such a compensative design process as having been put into practice industrially.

Besides that, this system may prevent continuous productions of failing printed circuit boards which will occur unless such a system available to identify and correct process errors. The printed circuit boards

fabricated with using this system will become a product having more efficiency per unit cost.

In the present invention, second conductor portion of quantifying conductors is in electrical contact with a common portion of trace means and first quantifying trace portion, and quantifying trace portion is positioned being parted from first conductor through a plurality of incrementally varying clearances. Hence, if the inner layer is out of registration with the exterior layer, the direction and amount of misregistration can be determined by sensing which and how many first conductive portions are in electrical contact.

Claims

1. A system for measuring the amount of misregistration of a panel comprising:

a plurality of layers each having electrically conductive traces which require interconnection between said layers including an exterior layer and a plurality of inner layers inwardly located relative to said exterior layer, said layers being positioned in registration with each other within a predetermined maximum tolerance range;

a plurality of electrically conductive quantifying conductor means for providing a path of electrical conductivity to said inner layer, said quantifying conductor means comprising first conductor portion including a plurality of conductors aligned along the virtual first axis crossing the measuring direction, and also at least one common conductor;

registration trace means formed upon said inner layer for cooperating with said quantifying conductor means to determine the direction and quantity of misregistration of said inner layer with said exterior layer, said trace means having a common portion adapted to be in electrical contact with said at least one common conductor whether or not said inner layer is in registration with said exterior layer, said trace means having a quantifying trace portion adapted to produce a plurality of incrementally varying clearances between said quantifying conductor means and said trace means if said inner layer is in perfect registration with said exterior layer;

said quantifying conductor being out of electrical contact with said trace means if said inner layer is in perfect registration with said exterior layer, and one or more of said quantifying conductors being in electrical contact with said trace means if said inner layer is out of registration with said exterior layer wherein electrical contact of said trace means with said first portion of conductors indicates misregistration in said measuring direction.

2. The system for measuring the amount of misregistration as described in claim 1, wherein a pair of said trace means are positioned in opposing relation with said first axis being formed there between, and the direction of misregistration of said inner layer with said exterior layer is determined by detecting trace means being in electrical contact.

3. The system for measuring the amount of misregistration as described in claim 1, comprising:

said electrically conductive means further having second portions of conductor including a plurality of conductors aligned along virtual second axis which is almost orthogonal to said first axis;

said trace means further having a quantifying trace portion adapted to produce a plurality of incrementally varying clearances between said second portion of conductor and said trace means if said inner layer is in perfect registration with said exterior layer;

the direction of misregistration of said inner layer with said exterior layer is determined by detecting a quantifying trace portion being in electrical contact.

4. The system for measuring the amount of misregistration as described in claim 1, comprising:

a plurality of layers each having electrically conductive traces which require interconnection between said layers, an exterior layer and a plurality of inner layers inwardly located relative to said exterior layer, said layers being positioned in registration with each other within a predetermined maximum tolerance range;

a plurality of electrically conductive quantifying conductor means for providing a path of electrical conductivity to said inner layer,

said quantifying conductor means comprising at least first and second conductor portions each said first and second conductor portions including a plurality of conductors aligned along common first and second axes respectively, said first portion of conductors being aligned in an arrangement substantially identical to said second portion of conductors, wherein said first axis is angled to said second axis, said quantifying conductor means further including at least one common conductor;

registration trace means formed upon said inner layer for cooperating with said quantifying conductor means to determine to determine the direction and quantity of misregistration of said inner layer with said exterior layer, said trace means having a common portion adapted to be in electrical contact with said at least one common conductor whether or not said inner layer is in reg-

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istration with said exterior layer, said trace means having a quantifying trace portion adapted to produce a plurality of incrementally varying clearances between said quantifying conductor means and said trace means if said inner layer is in perfect registration with said exterior layer;

said quantifying conductors being out of electrical contact with said trace means if said inner layer is in perfect registration with said exterior layer, and one or more of said quantifying conductors being in electrical contact with said trace means if said inner layer is out of registration with said exterior layer wherein electrical contact of said trace with said first portion of conductors indicates misregistration in a first direction and electrical contact between said trace means and said second portion of conductors indicates misregistration in a second direction.

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5. The system for measuring the amount of misregistration as described in claim 4, comprising:

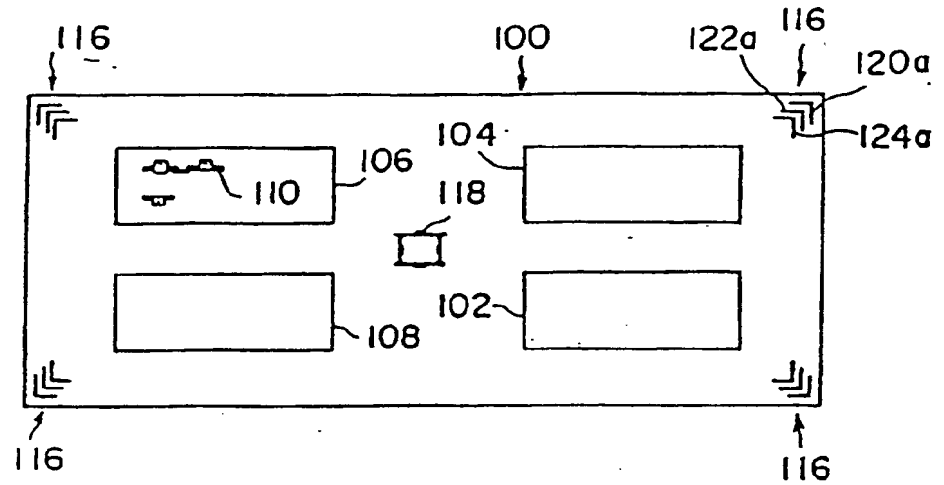
tester means for determining the electrical contact between said plurality of conductors and said at least one common conductor, said tester means including a plurality of probes for contacting said conductors and said at least one common conductor for determining if a conductive pass exists between any one of said conductors of said first and second conductor portions and said at least one common conductor.

6. The system for measuring the amount of misregistration as described in claim 5, wherein said at least one common conductor comprises first and second common conductors and wherein said trace means further comprises first and second trace portions adapted to cooperate with said first and second plurality of conductors, respectively, to determine the direction and quantity of misregistration of said inner layer in the first and second directions.

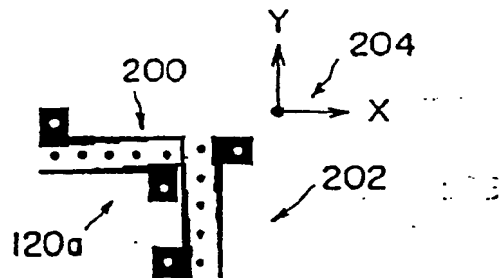
7. The system for measuring the amount of misregistration as described in claim 5, wherein said at least one common conductor comprises first, second, third and fourth common conductors and wherein said trace means further comprises first, second, third and fourth trace portions, said first and second trace portions being electrically coupled to said first and second common conductors, respectively, and adapted to cooperate with said first portion of conductors to determine the quantity of misregistration in the first direction and said third and fourth trace portions being electrically coupled to said third and fourth common conductors, respectively, and adapted to cooperate with said second portion of conductors to determine the quantity of misregistration in the second direc-

- tion.
8. The system for measuring the amount of misregistration as described in claim 7, wherein said first and second trace portions are positioned in opposing relation with said first portion of conductors being formed therebetween. 5
 9. The system for measuring the amount of misregistration as described in claim 8, wherein said third and fourth trace portions are positioned in opposing relation with said second portion of conductors being formed therebetween. 10
 10. The system for measuring the amount of misregistration as described in claim 6, wherein each said first, second, third and fourth trace portion comprises a plurality of elongate trace conductor segments having varying thickness such that the variation in the thickness of each said conductor segment provides the incremental variation in clearance between said quantifying conductor and said first trace means, each said trace conductor segment being electrically coupled its respective common conductor. 15
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 11. The system for measuring the amount of misregistration as described in claim 10, wherein the length of each said conductor segment is selected to be larger than the maximum expected misregistration to ensure the integrity of the incrementally increasing clearance. 25
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 12. The system for measuring the amount of misregistration as described in claim 5, further comprising a second inner layer, second qualifying conductor means for providing a path of electrical conductivity to said second inner layer, and second trace means for cooperating with said second quantifying conductor means to determine the quantity and direction of misregistration of said second inner layer in the first and second directions. 35
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 13. The system for measuring the amount of misregistration as described in claim 5, wherein said first and second plurality of quantifying conductors are positioned in orthogonal relation to determine the misregistration in directions which are orthogonal to one another. 45
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 14. The system for measuring the amount of misregistration as described in claim 5, wherein said first and second plurality of quantifying conductors are formed along substantially parallel axes to determine the misregistration in directions which are opposite to one another. 55
 15. The system for measuring the amount of misregistration as described in claim 5, wherein said trace means is positioned along a perimeter portion of said inner layer, outward from said traces thereon and out of electrical contact with said traces thereon.
 16. A method of determining misalignment between two layers of a circuit board arrangement, each of which carries a respective pattern of conductive elements, the method comprising detecting the presence of an electrical contact between the two patterns, thereby to determine said misalignment.
 17. A method as claimed in claim 16, wherein one of said patterns comprises a plurality of conductors aligned in a direction transverse to a measuring direction, the method further comprising determining the presence of an electrical contact established between one of said conductors and the other of said patterns, thereby to determine the sense of misalignment in the measuring direction.
 18. A method as claimed in claim 16 or claim 17, wherein said patterns are such as to define a plurality of incrementally varying nominal clearances therebetween, when the layers are perfectly aligned, the method further comprising determining quantitatively the extent of the misalignment, when the layers are not in perfect alignment, by determining which of said nominal clearances are not present.

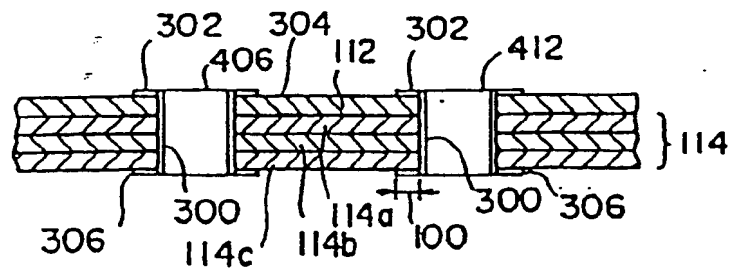
[FIG. 1]



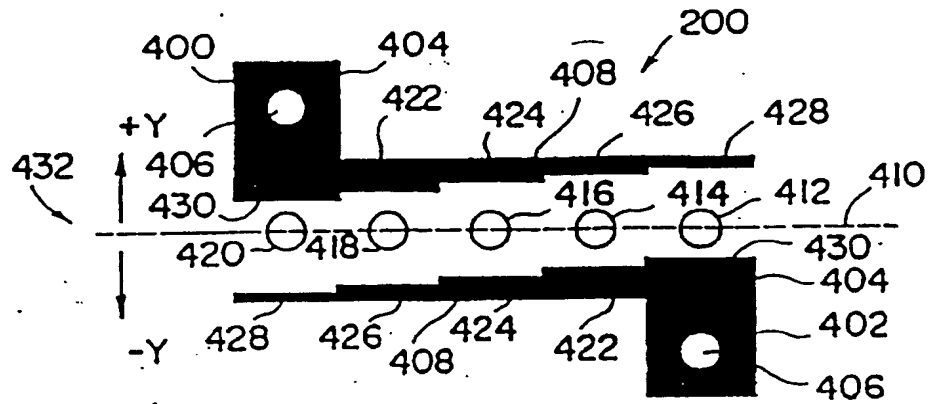
[FIG. 2]



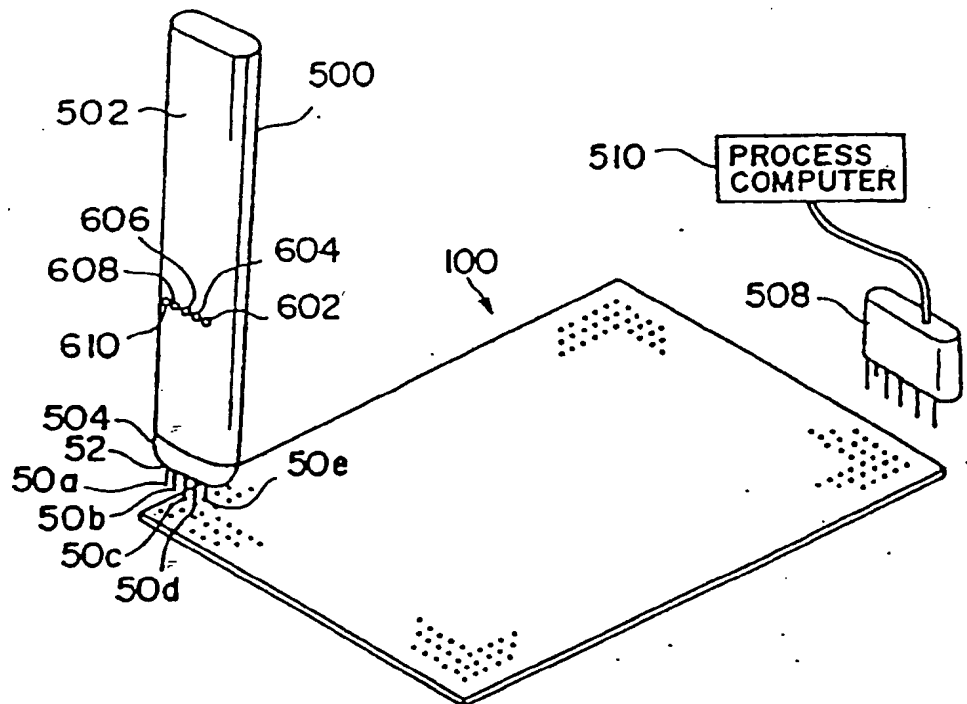
[FIG. 3]

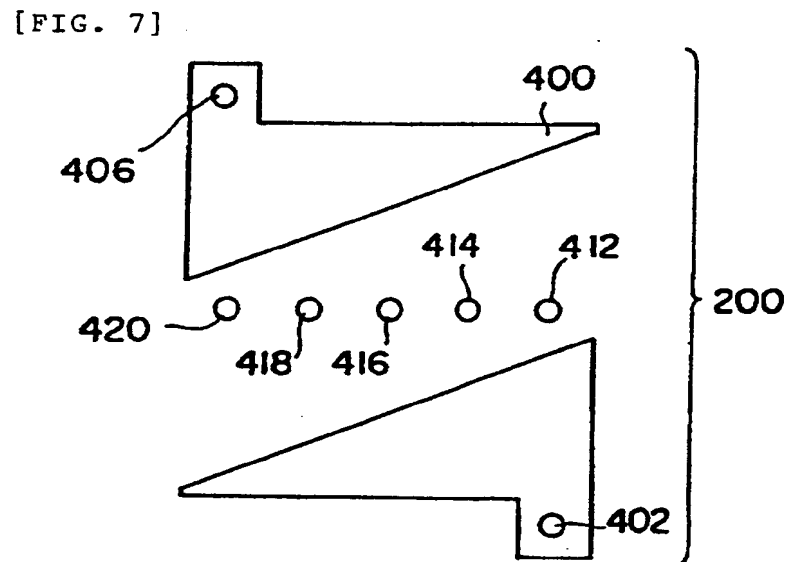
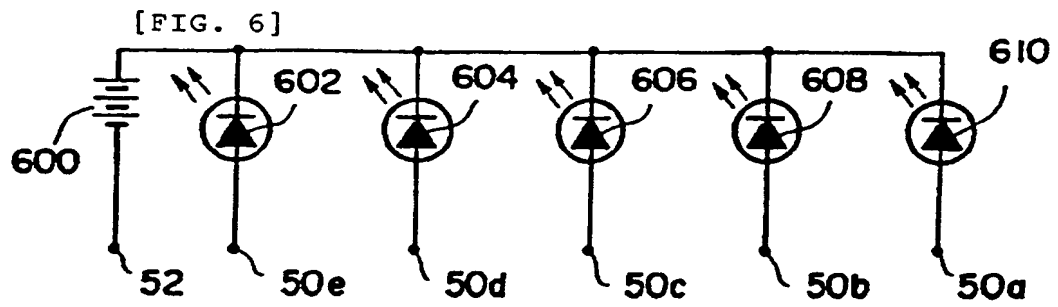


[FIG. 4]

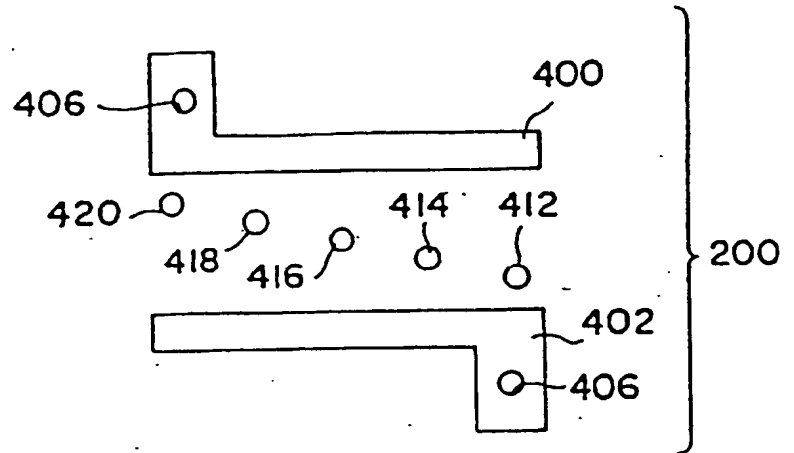


[FIG. 5]

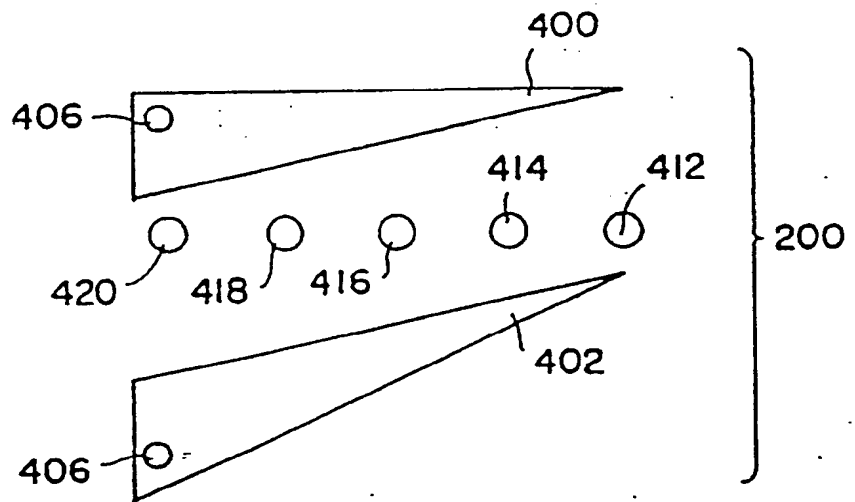




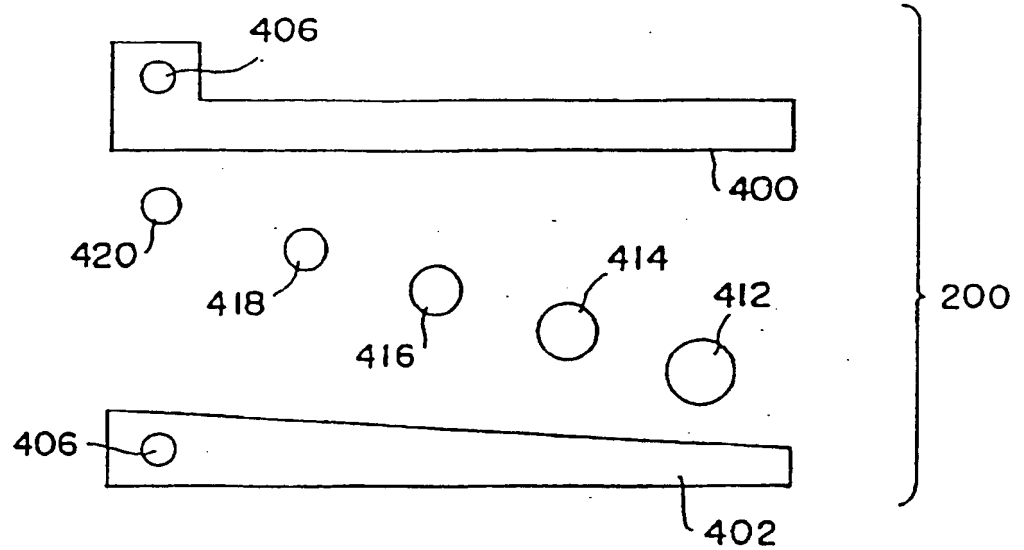
[FIG. 8]



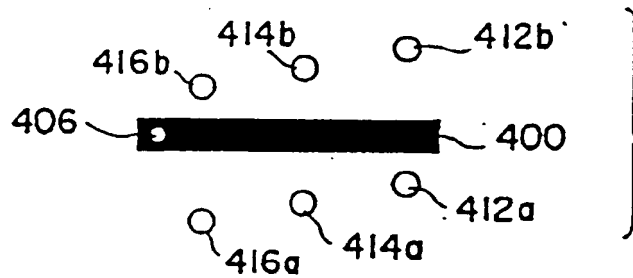
[FIG. 9]



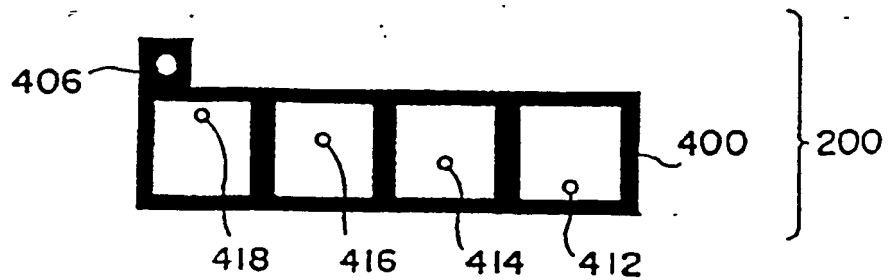
[FIG. 10]



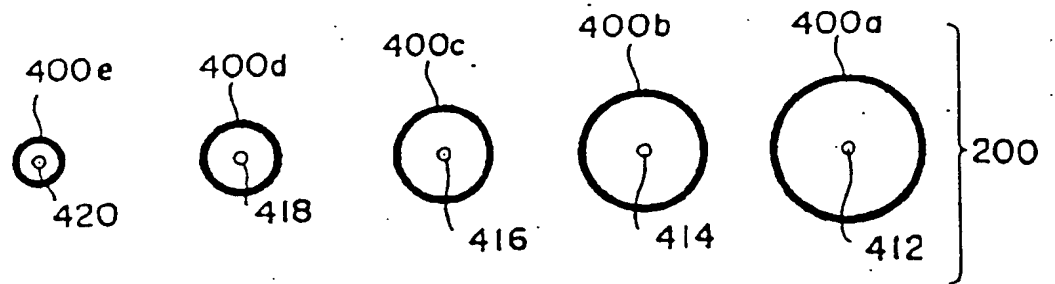
[FIG. 11]



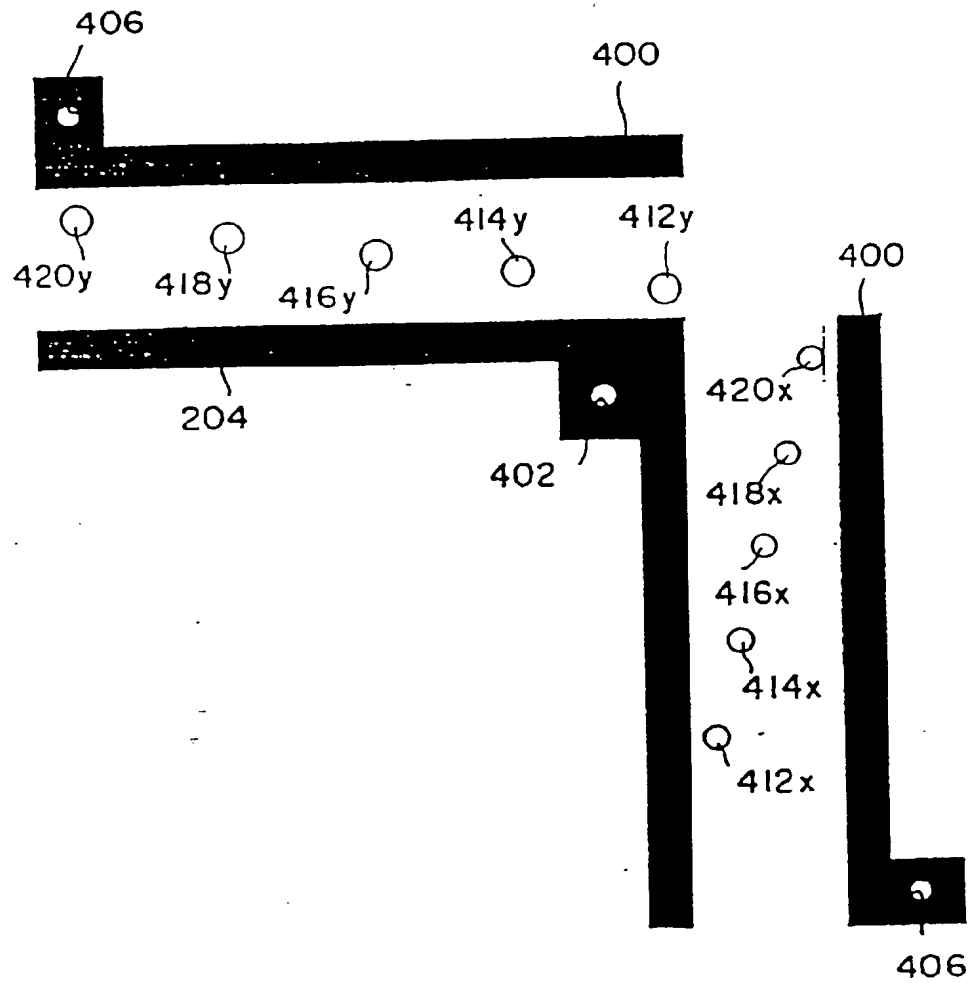
[FIG. 12]



[FIG. 13]



[FIG. 14]



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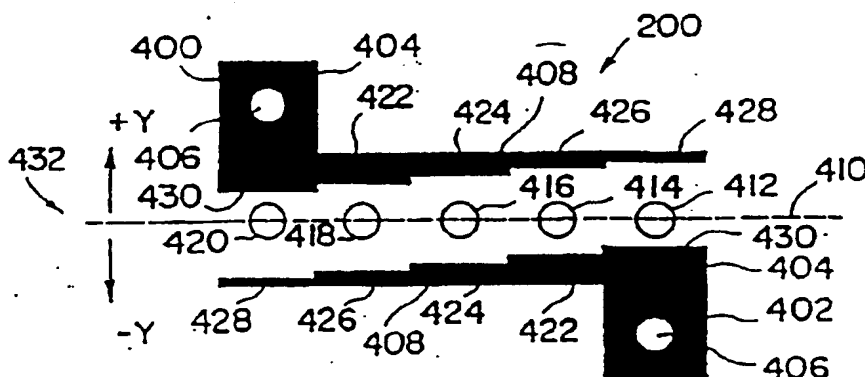
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(54) **System for measuring misregistration.**

(57) The present invention relates to a system for measuring the amount and direction of misregistration of each layer of a panel from which multi-layer printed circuit boards are separated. The system utilizes a conductor pattern having a conductive trace positioned on each layer of the panel. Each conductive trace includes two trace portions 400 and 402, positioned in opposing relation with a plurality of conductive through-holes 412, 414, 416, 418, and 420, being positioned therebetween. Each of the trace portions 400 and 402 includes a plurality of elongate conductor portions 408 positioned incrementally varying distances from a common axis. The conductive through-holes 412, 414, 416, 418, and 420 are formed in the panel after the layers have been fabricated and joined. If the through-holes 412, 414, 416, 418 and 420 are in electrical contact with either trace portion of 400 or 402, then the amount of misregistration can be determined. Further, by determining which trace portion the through-hole is in contact with, the direction of misregistration can be determined. By adding additional trace portions, the quantity of misregistration in other directions may be determined.

[FIG. 4]



EP 0 557 136 A3



European Patent
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EUROPEAN SEARCH REPORT

Application Number
EP 93 30 1267

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.5)
X,D A	US-A-4 894 606 (PAUR) * abstract * * column 9, line 40 - column 10, line 13; figures 1-5,10 *	16 1	G01R31/28
X A	EP-A-0 053 272 (SIEMENS) * abstract * * page 6, line 23 - page 7, line 16; figures 1,4-6 *	16 2-18	
X A	FR-A-2 641 932 (THOMSON-CSF) * claims 1,2,9; figures 4,6 *	16 1	
X A	US-A-4 510 446 (BRAUN ET AL.) * column 2, line 50 - column 3, line 11; figures 1-4 *	16 1	
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int.Cl.5) G01R H05K
Place of search THE HAGUE		Date of completion of the search 3 December 1993	Examiner Iwansson, K
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